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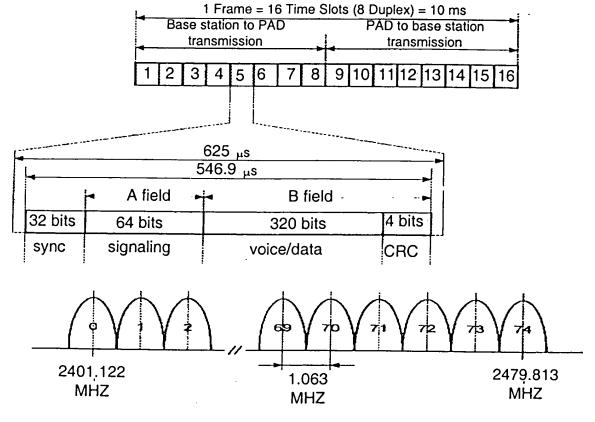


Fig. 7

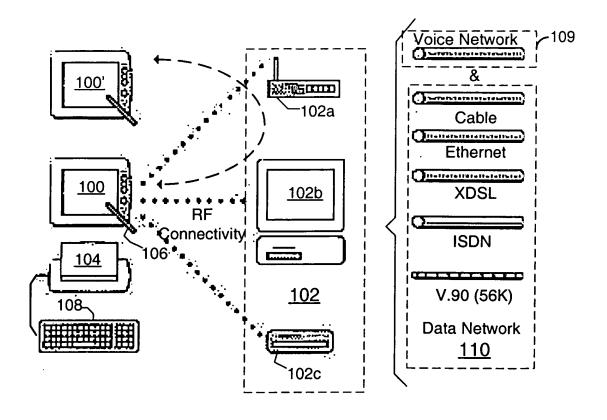


Fig. 2

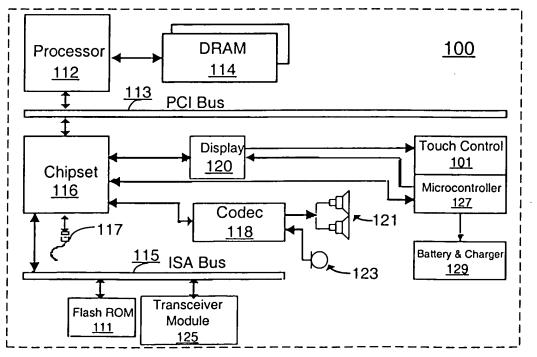


Fig. 3

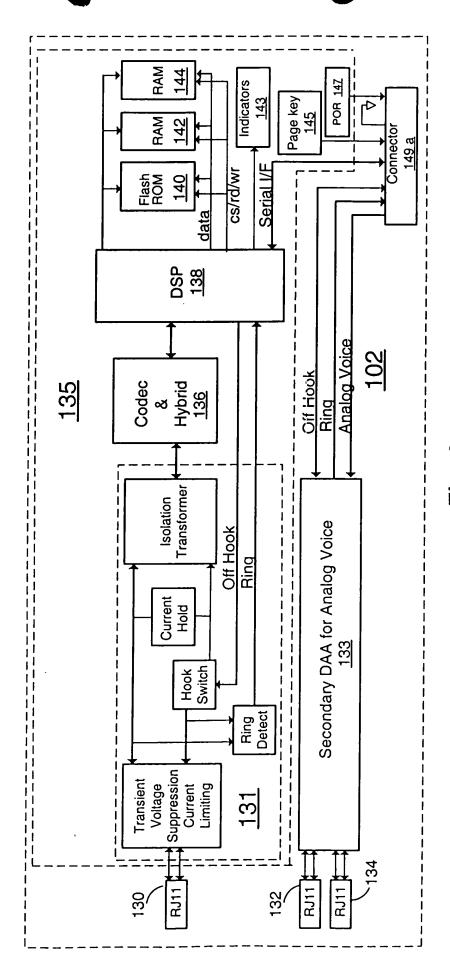


Fig. 4

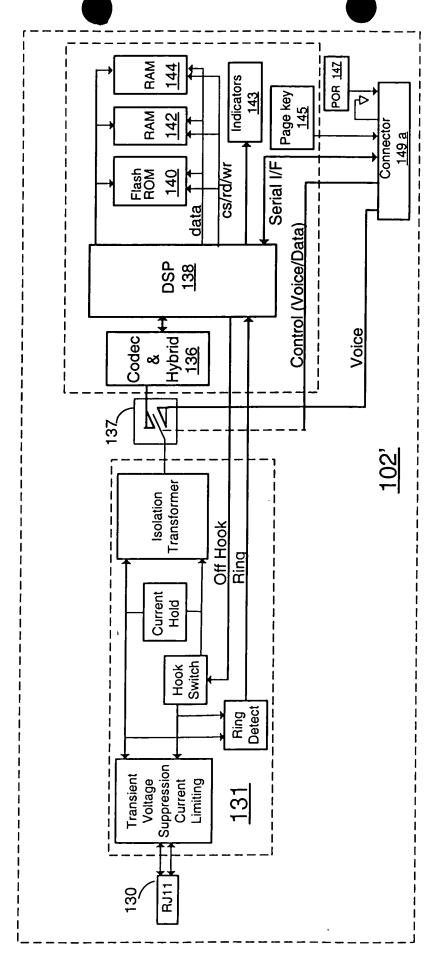


Fig. 5

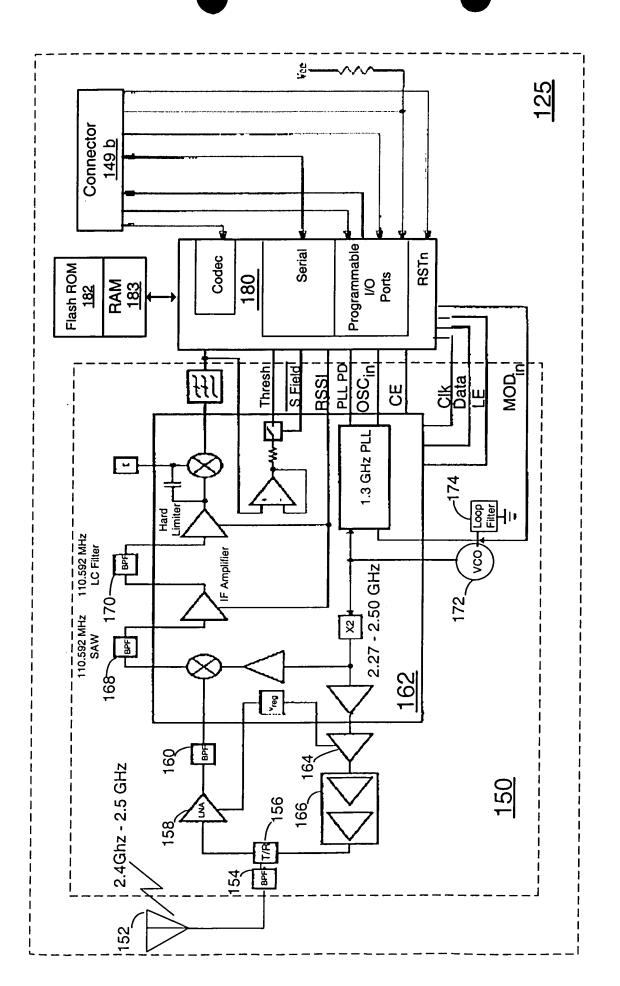
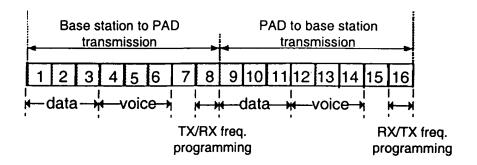


Fig. 6



| 4 | | 625 rs | | | | | | |
|----------|-----------|---------|----------|--------|--|--|--|--|
| - | 546.9µs | | | | | | | |
| | ■ A field | + | B field | · | | | | |
| 32 bits | 64 bits | 80 bits | 240 bits | 4 bits | | | | |
| sync | signaling | FECC | data | CRC | | | | |

| - | | | | 5 rs | · · · · · · · · · · · · · · · · · · · | -► | |
|---|---------|-----------|---|----------|---------------------------------------|----|--|
| | 546.9µS | | | | | | |
| | | A field | + | B field | · 🛌 | | |
| | 32 bits | 64 bits | | 320 bits | 4 bits | | |
| | sync | signaling | | voice | CRC | , | |

Fig. 8

| PREAMBLE | | HEADER | | | DATA | | |
|-------------------------|----------------------------|--------------------------------|--|--------------------------------|-------------------------|--|--|
| SYNC WORD 80 BITS | FRAME TIMING 16 BITS | DATA WORD LENGTH 12 BITS | | ERROR CHECK WORD 16 BITS | DATA 1-4095 BYTES | | |
| 128 BITS | | | | | | | |

Fig. 9 (Prior Art)